RKUNIVERSITY

SYLLABUS

Course Title	**Digital Circuit Design**
Course Code	**EC201**
Course Credit	Theory : 03
	Practical : 01
	Tutorial : 00
	Credits : 04

Course Learning Outcomes

After completing the course students will be able to

- **Apply** knowledge of Boolean algebra to Digital Circuit minimization.
- **Design** digital systems from component (gate) level to meet desired needs.
- **Identify**, **formulate** and **solve** engineering problems related to digital system design using project-based learning approach.

Detailed Syllabus

Sr. No.	Name of chapter & details	Hours Allotted
	Section – I	
1	**Boolean Algebra and Logic Gates** Basic Definition, Axiomatic Definition of Boolean Algebra, Basic Theorem and Properties of Boolean Algebra, Min terms And Max terms, Logic Operations, Digital Logic Gates, IC digital Logic Families	**05**
2	**Simplification of Boolean Functions** Different types Map method, Product of sum Simplification, NAND or NOR implementation, Don't Care condition, Tabulation method	**07**
3	**Combinational Logic** Introduction, Design Procedure, adder, subtractor, Code Conversion, Universal gate	**04**
4	**Combinational Logic With MSI AND LSI** Introduction, Binary Parallel Adder, Decimal Adder, Magnitude Comparator, Decoder and Encoder, Multiplexer and De-multiplexer	**05**
	Section – II	
5	**Binary System** Digital computer and digital systems, Binary Number, Number base conversion Octal and Hexadecimal Number, complements, Binary Codes, Binary Storage and register, Binary Logic, Integrated Circuit	**06**

6	**Sequential Logic** Introduction, Flip-Flops, Triggering of Flip-Flops, Analysis of Clocked Sequential Circuits, State Reduction and Assignment, Flip-Flop Excitation Tables, Design Procedure, Design of Counters, Design with State Equations	**07**
7	**Registers, Counters and the Memory unit** Introduction, Registers, Shift Registers, Ripple Counters, Synchronous Counters, Timing Sequences	**05**
8	**Processor Logic Design** Introduction, Processor Organization, Arithmetic Logic Unit, Design of Arithmetic and logic circuit, Design of ALU. Status Register, Design of shifter, Processor Unit, Design of Accumulator.	**04**

Instructional Method and Pedagogy:

- Lectures will be conducted with the aid of multi-media projector, blackboard, OHP etc. Assignments based on course contents will be given to the students at the end of each unit/topic and will be evaluated at regular interval
- Minimum five experiments shall be there in the laboratory related to course contents
- Minimum six tutorials which includes solution of minimum five computer programs in each head

List of Experiments

Sr. No.	Name of Experiments
1.	To **perform** operation of all logic gates.
2.	To **implement** NAND gate and NOR gate as universal gate.
3.	To **perform** the arithmetic function by building Half Adder, Full Adder, Half Subtractor and Full subtractor circuit using logic gates.
4.	To **perform** Binary to Gray and Gray to Binary Code Conversion.
5.	To **understand** the concept of Seven-Segment Display Decoder.
6.	To **implement** 4-bit magnitude comparator using 74LS85.
7	To **perform** and **verify** the truth table of different types of Flip Flop.
8.	To **understand** and **implement** synchronous and asynchronous Counters.
9.	To **perform** and **understand** the operation of Multiplexer and De-multiplexer.
10	To **perform** and **understand** the operation of Encoder and Decoder.
11	To **understand** TTL (74LS00) and CMOS (74HC00) transfer characteristics.
12	To **understand** about Digital IC Tester.

Reference Books:

1. Digital Logic and Computer Design By M Morris Mano
2. Fundamental of digital circuits By A. Anand kumar
3. Principle of digital Electronics By Malvino & Leach
4. Modern Digital Electronics By R.P.Jain

Additional Resources

- http://bwrc.eecs.berkeley.edu/classes/icbook/SLIDES/slides4.pdf
- http://www.wiley.com/college/engin/balabanian293512/pdf/ch04.pdf
- http://www.electronics-tutorials.ws
- http://www.csee.umbc.edu/~cpatel2/links/640/lectures/lect17_seq.pdf